PIANIFICAZIONE TRAINING XILINX Q2 2014

Data (*)	Durata	Corso "Public" (ILT)	Costo	TC
1 - 2	2	Designing with 7 Series	€ 1600 + IVA	18
4	1	Vivado Design Suite Hands-on Introductory Workshop	€ 800 + IVA	9
8 - 9	2	Advanced Embedded Systems Development	€ 1600 + IVA	18
10 - 11	2	Essentials of FPGA Design	€ 1600 + IVA	18
15	1	Advanced Features & Techniques of Embedded Systems Software Design	€ 800 + IVA	9
16-17-18	3	Vivado Design Suite Static Timing Analysis and Design Constraints	€ 2400 + IVA	27
23 - 24	2	Minimizing Your Design Time with the ChipScope Pro Debug and Verification Tools	€ 1600 + IVA	18
28 - 29	2	Advanced Tools & Techniques of Vivado Design Suite	€ 1600 + IVA	18

Aprile 2014

L	M	M	G	V	S	D
	1	**2**	3	**4**	5	6
7	**8**	**9**	**10**	**11**	12	13
14	**15**	**16**	**17**	**18**	19	20
21	22	**23**	**24**	25	26	27
28	**29**	30				

Data (*)	Durata	Corso "Public" (VILT)	Costo	TC
1 > 4	4	Essentials of FPGA Design	€ 1600 + IVA	18
8 - 9	2	Advanced Features & Techniques of Embedded Systems Software Design	€ 800 + IVA	9
14 > 18	5	Vivado Design Suite Static Timing Analysis and Design Constraints	€ 2400 + IVA	27
29 - 30	2	Introduction to Zynq EPP Architecture	€ 800 + IVA	9

Aprile 2014

L	M	M	G	V	S	D
	1	**2**	**3**	**4**	5	6
7	**8**	**9**	10	11	12	13
14	**15**	**16**	**17**	**18**	19	20
21	22	23	24	25	26	27
28	**29**	**30**				

Mindway Srl ~ Via Pordoi 8 ▪ 20019 Settimo Milanese (MI) ~ Tel. +39 02 33549098 ▪ Fax +39 02 33503438 ▪ P.Iva 04902830969
E-mail: training@mindway-design.com

Data (*)	Durata	Virtual Training (ILT)	Costo	TC
5 - 6 - 7	3	Designing with VHDL	€ 1800 + IVA	27
8 - 9	2	Designing with 7 Series	€ 1600 + IVA	18
12 - 13	2	Embedded Systems Development	€ 1600 + IVA	18
15 - 16	2	Essentials of FPGA Design	€ 1600 + IVA	18
20-21-22	3	Vivado Design Suite Static Timing Analysis and Design Constraints	€ 2400 + IVA	27
26 - 27	2	Embedded Systems Software Development	€ 1600 + IVA	18
29 - 30	2	Minimizing Your Design Time with the ChipScope Pro Debug and Verification Tools	€ 1600 + IVA	18

Maggio 2014

L	M	M	G	V	S	D
			1	2	3	4
5	**6**	**7**	**8**	**9**	10	11
12	**13**	14	**15**	**16**	17	18
19	**20**	**21**	**22**	23	24	25
26	**27**	28	**29**	**30**	31	

Data (*)	Durata	Corso "Public" (VILT)	Costo	TC
6 > 9	4	Essentials of FPGA Design	€ 1600 + IVA	18
13 > 16	4	Embedded Systems Development	€ 1600 + IVA	18
19 > 23	5	Vivado Design Suite Static Timing Analysis and Design Constraints	€ 2400 + IVA	27
27 > 30	4	Embedded Systems Software Development	€ 1600 + IVA	18

Maggio 2014

L	M	M	G	V	S	D
			1	2	3	4
5	**6**	**7**	**8**	**9**	10	11
12	**13**	**14**	**15**	**16**	17	18
19	**20**	**21**	**22**	**23**	24	25
26	**27**	**28**	**29**	**30**	31	

Data (*)	Durata	Virtual Training (ILT)	Costo	TC
4 - 5	2	Designing with 7 Series	€ 1600 + IVA	18
6	1	Vivado Design Suite Hands-on Introductory Workshop	€ 800 + IVA	9
9 - 10	2	Advanced Embedded Systems Development	€ 1600 + IVA	18
12 - 13	2	Essentials of FPGA Design	€ 1600 + IVA	18
16	1	Advanced Features & Techniques of Embedded Systems Software Design	€ 800 + IVA	9
18-19-20	3	Vivado Design Suite Static Timing Analysis and Design Constraints	€ 2400 + IVA	27
24	1	Introduction to Zynq EPP Architecture	€ 800 + IVA	9
26 - 27	2	Minimizing Your Design Time with the ChipScope Pro Debug and Verification Tools	€ 1600 + IVA	18

Giugno 2014

L	M	M	G	V	S	D
						1
2	3	**4**	**5**	**6**	7	8
9	**10**	11	**12**	**13**	14	15
16	17	**18**	**19**	**20**	21	22
23	**24**	25	**26**	**27**	28	29
30						

Data (*)	Durata	Virtual Training (VILT)	Costo	TC
5 - 6	2	Vivado Design Suite Hands-on Introductory Workshop	€ 800 + IVA	9
10 > 13	4	Essentials of FPGA Design	€ 1600 + IVA	18
17 > 20	4	Embedded Systems Development	€ 1600 + IVA	18
23 > 27	5	Vivado Design Suite Static Timing Analysis and Design Constraints	€ 2400 + IVA	27

Giugno 2014

L	M	M	G	V	S	D
						1
2	3	4	**5**	**6**	7	8
9	**10**	**11**	**12**	**13**	14	15
16	**17**	**18**	**19**	**20**	21	22
23	**24**	**25**	**26**	**27**	28	29
30						

(*) Mindway si riserva il diritto di modificare le date o di cancellare i corsi già pianificati in caso di insufficiente numero di iscritti.

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E-mail: training@mindway-design.com